

EL465851515

1 Priority Application Serial No. .... 09/141,775  
2 Priority Filing Date ..... August 27, 1998  
3 Inventor ..... Guy T. Blalock et al.  
4 Assignee ..... Micron Technology, Inc.  
5 Priority Group Art Unit ..... 1765  
6 Priority Examiner ..... L. Vinh  
7 Attorney's Docket No. .... MI22-1544  
8 Title: Plasma Etching Methods



6 INFORMATION DISCLOSURE STATEMENT  
7 PURSUANT TO 37 C.F.R. §§1.56, 1.97 AND 1.98

8 In compliance with 37 C.F.R. §§1.56, 1.97 and 1.98, your attention is  
9 directed to the United States patents and other references listed on the  
10 attached Form PTO-1449.

11 The listed references were cited by, or submitted to, the Office in the  
12 parent, co-pending application of the above-identified application. The above-  
13 identified application is a continuation of co-pending application Serial  
14 No. 09/141,775, filed August 27, 1998. Such prior disclosure is sufficient for  
15 the above-identified application as far as copies of the references are  
16 concerned. 37 C.F.R. §1.98(d) and MPEP §609(2). No admission is made  
17 regarding whether all the submitted references are prior art.

18 Citation of these references is respectfully requested.

19  
20 Dated: Oct 2, 2000

Respectfully submitted,  
21 Bernard Berman  
22 Attorney: Bernard Berman  
Reg. No. 37,279  
WELLS, ST. JOHN, ROBERTS,  
GREGORY & MATKIN P.S.

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1544		SERIAL NO.	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Guy T. Blalock et al.		GROUP 1765 (priority)	
U.S. PATENT DOCUMENTS				FILING DATE Filed herewith			
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,879,575	8/98	Tepman et al.			
	AB	5,868,853	2/99	Chen et al.			
	AC	5,830,279	11/98	Hackenberg			
	AD	5,817,578	11/98	Ogawa			
	AE	5,788,869	8/98	Dalton et al.			
	AF	5,756,400	5/98	Ye et al.			
	AG	5,681,424	10/97	Saito et al.			
	AH	5,679,215	10/97	Barnes et al.			
	AI	5,644,153	7/97	Keller			
	AJ	5,626,775	5/98	Roberts et al.			
	AK	5,468,686	11/95	Kawamoto			
	AL	5,780,338	7/98	Jeng et al.			
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AM						
	AN						
	AQ						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AR		Stanley Wolf and Richard Tauber, Silicon Processing for the VLSI Era, Vol. 1, pp 549, 1986.				
	AS						
	AT						
EXAMINER				DATE CONSIDERED			
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>							

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FILING DATE Filed herewith		APPLICANT Guy T. Blalock et al.			
GROUP 1765 (priority)					

  

U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
AA	4,513,021	4/85	Purdes et al.				
AB	5,873,948	2/99	Kim				
AC	5,843,239	12/98	Shrotriya				
AD	5,716,494	2/98	Imai et al.				
AE	5,593,540	1/97	Tomita et al.				
AF	5,514,247	5/96	Shan et al.				
AG	5,242,538	9/93	Hamrah et al.				
AH	5,681,424	10/97	Saito et al.				
AI	4,528,066	7/85	Merkling, Jr. et al.				
AJ	5,843,226	12/98	Zhao et al.				
AK	6,093,655	7/00	Donohoe et al.				
AL	09/080,656		Tuman Earl Allen III				5/18/1998

  

FOREIGN PATENT DOCUMENTS							
Document Number	Date	Country	Class	Subclass	Translation		
					Yes	No	
AM							
AN							
AO							
AP							
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)			
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U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	09/429,880	10/99	Donohoe et al.			10/29/99
	AB	5,935,340	8/99	Xia et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
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